## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

§ §

§

§

§

S

§

S

S § S

re application of: Slemmer

Serial No.: 08/056,301

Filed: April 30, 1993

DIRECT CURRENT SUM For: VOLTAGE BANDGAP COMPARATOR

2504 Group Art Unit:

Examiner: Cunningham,

Attorney Docket No.: 93-C-07

Certificate of Mailing Under 37 C.F.R. § 1.8(a)

I hereby certify this correspondence is being deposited with the United States Postal Service as First Class mail in an envelope addressed to: Commissioner of Patents and Trademarks,

Washington, D.C. 20231 on July 5, 1995.

## RESPONSE TO OFFICE ACTION

Honorable Commissioner of Patents And Trademarks Washington, D.C. 20231

RECEIVED JUL 2 7 1995 **GROUP 2500** 

Sir:

No fees are believed to be necessary; however, in the event that any fees are required for the prosecution of this application, please charge any necessary fees to Deposit Account No. 06-0580. No extension of time is believed to be If, however, an extension of time is needed, the necessary. extension is requested and please charge the fee for this extension to Deposit Account No. 06-0580.

In response to the Office Action dated April 4, 1995 please amend the above-identified application as follows: IN THE SPECIFICATION:

Please replace the abstract with the following:

- A direct current sum bandgap voltage comparator for detecting voltage changes The direct current sum in a power supply. bandgap voltage comparator includes a summing node, current sources connected to the summing node and the power supply, and an indicator circuit connected to the summing node. Each current source in the supplies a current to the supplies a current to the supplies in wherein the summing node voltage level is

